	Class Subclass	ISSUE CLASSIFICATION					PATENT NUMBER
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			SCANNED _	O.I.P.E.	PATEN	IT DATE	
 	APPLICATION NO.	CONT/PRIOR	CLASS	Cumpu			
	09/955270		327	SUBCLASS 392	2816 28/5	EXAMINER	
	Christopher	Chevall	ier				
Į.	Supply volt	age redu	stion cir	cuit for i	ntegrated	circuit	
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ISSUING CLASSIFICATION										
ORIGINA	L.		CROSS REFERENCE(S)							
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INTERNATIONAL	CLASSIFICATION									
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TERMINAL	DRAWINGS			CLAIMS ALLOWED		
☐ DISCLAIMER	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.	
The term of this patent				NOTICE OF ALLOWANCE MAILED		
subsequent to (date) has been disclaimed.	(Assistant Examiner) (Date)					
The term of this patent shall not extend beyond the expiration date				: :	•	
of U.S Patent. No.				ISSUE FEE		
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this patent have been disclaimed.	(Legal Instrume	ents Examiner)	(Date)			
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orm PTO-436A Rev. 6/99)			FILED WITH:	DISK (CRF)	FICHE CD-RO	

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